Researcher's Information

Name : Ir. Ts. Dr. Mohd Azlan Abu
E-mail : mohdazlan@unikl.edu.my
Expertise : Embedded Systems, FPGA, Integrated Circuit Design, Radio Over Fiber, Data Science, IoT, Big Data & Analytics, Artificial Intelligence.

Publication Links

Google Scholar : https://scholar.google.com/citations?user=qvA7KRIAAAAJ&hl=en
ResearchGate : https://www.researchgate.net/profile/Mohd_Azlan_Abu
SCOPUS ID : https://www.scopus.com/authid/detail.uri?authorId=55604690200
ORCID : http://orcid.org/0000-0001-9271-2635

Grants

No	Grants Name	Project Title	Member / Leader	Value (RM)	Year
1	STRG	The Design and Development of STTC Viterbi Decoder by using Intel FPGA Complex Programmable Logic (CPLD) for Low Power Consumption	Leader	20,000	2017 - Present
2	STRG	Radio Over Fiber System	Leader	20000	2011 - 2013

Current Supervision

No	Level	Name	Main/ Co SV	Title
1				
2				
3				

Membership

1. Professional Engineer, P.Eng (Board of Engineers Malaysia)
2. Professional Technologist, P.Tech (Malaysia Board of Technologists)
3. Senior Member, Institute of Electrical and Electronics Engineers (IEEE)
4. Corporate Member of The Institution of Engineers Malaysia (IEM)
5. Member of The Institution of Engineering and Technology (IET)

Qualifications:

PhD in Electronic Engineering, UPM (2018)

M.Eng in Electrical, Electronics & Telecommunications, UTM (2010)

B.Eng in Electrical & Electronics, UTM (2008)